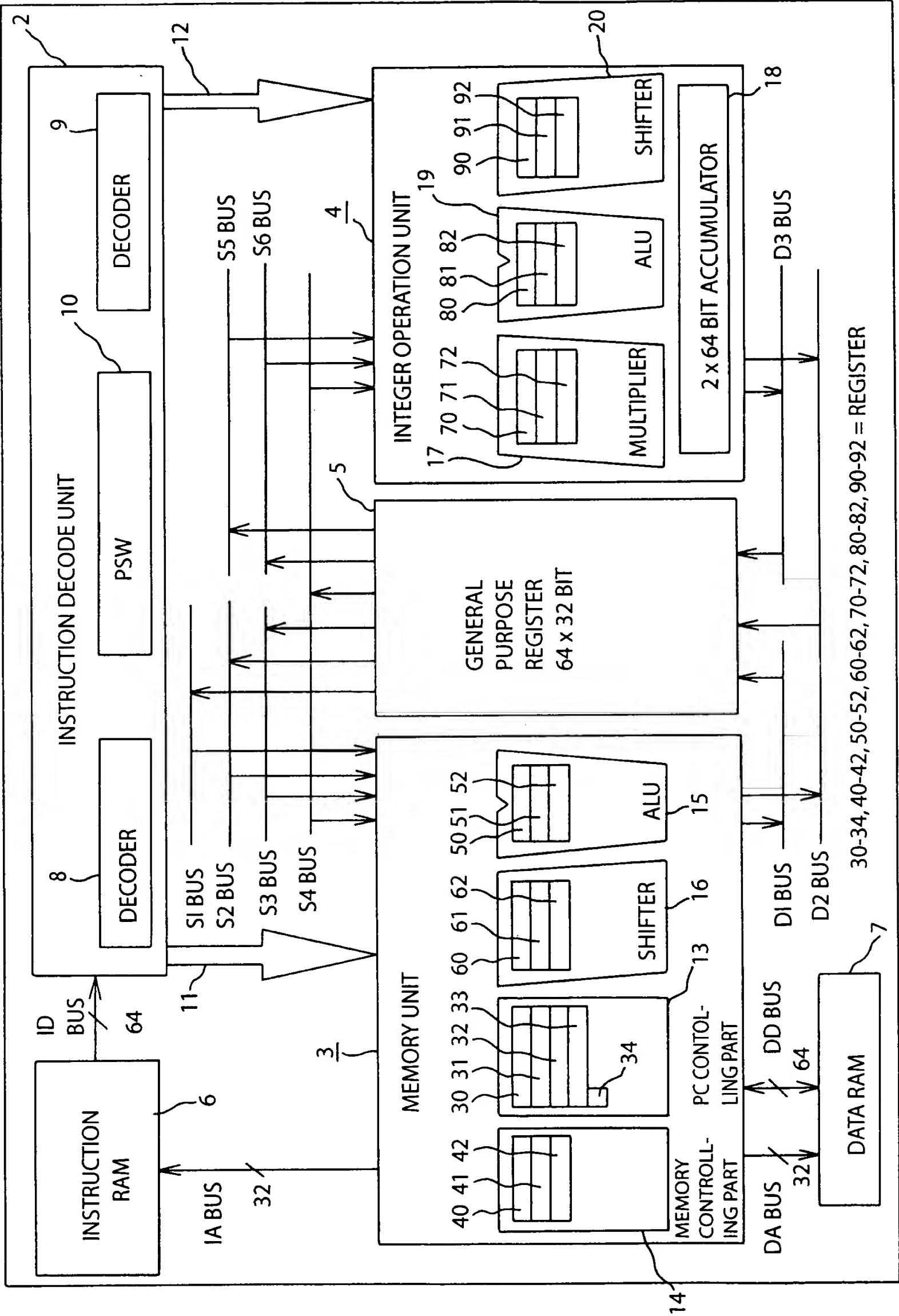
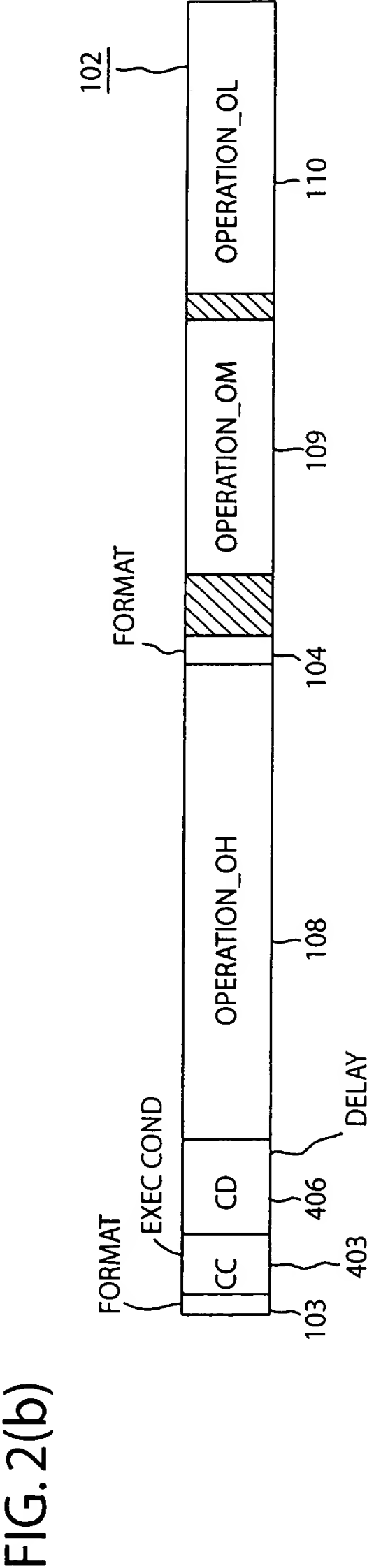
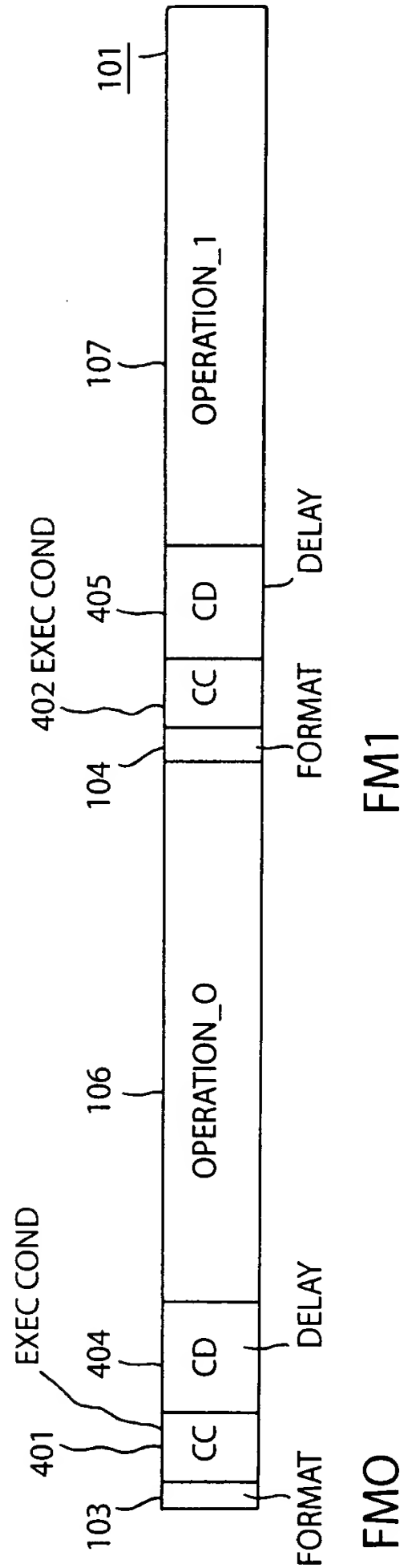




FIG. 1





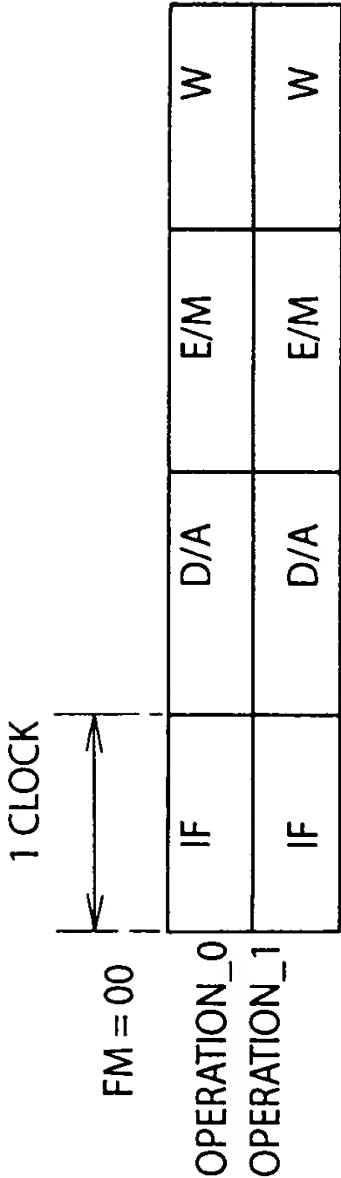


FIG. 3 (a)

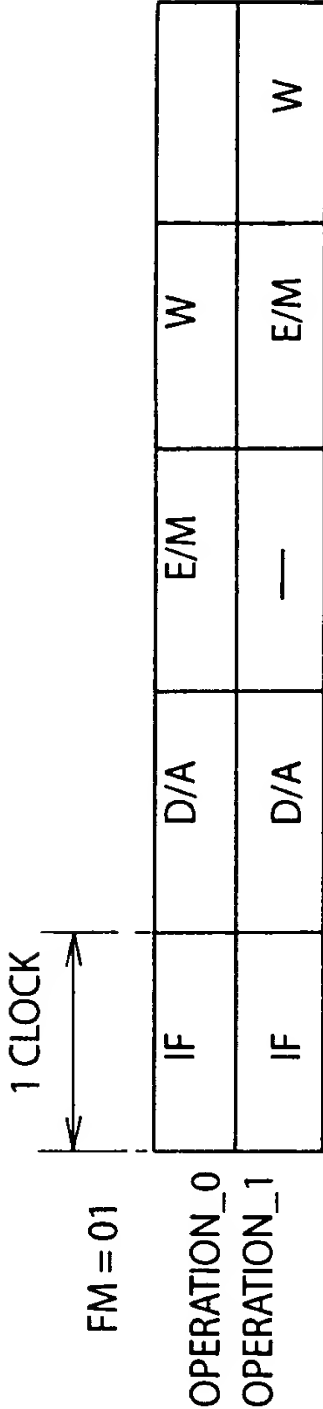


FIG. 3 (b)

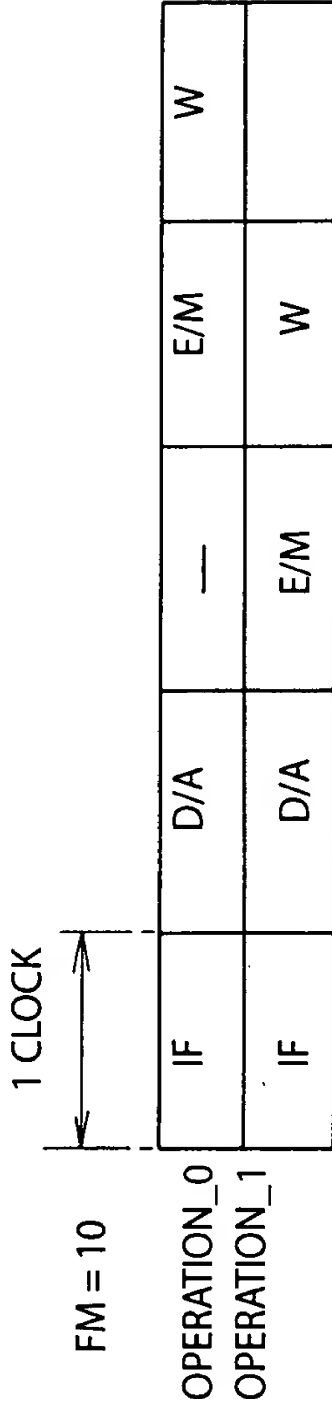
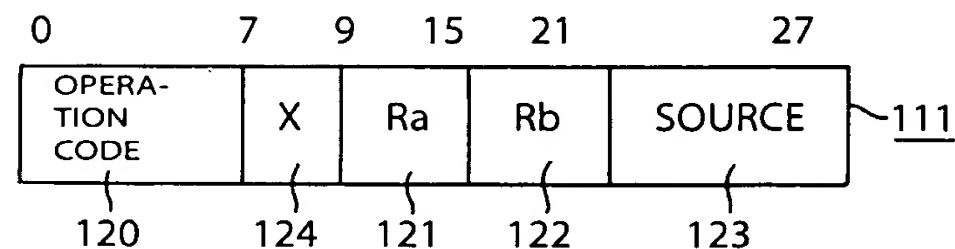
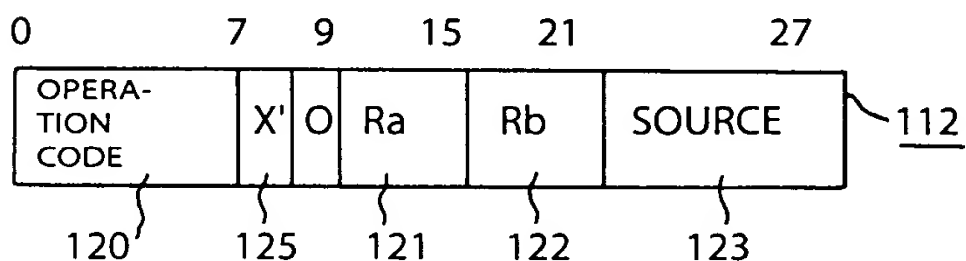


FIG. 3 (c)

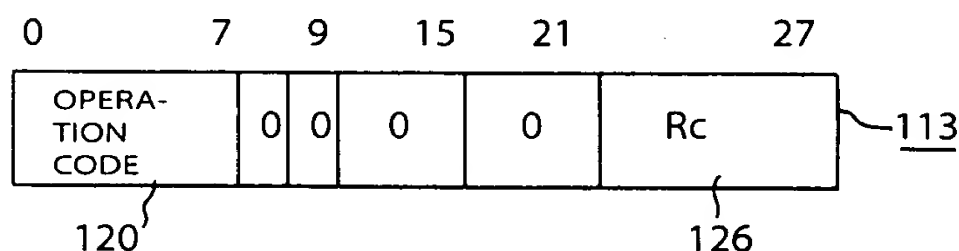
FIG. 4



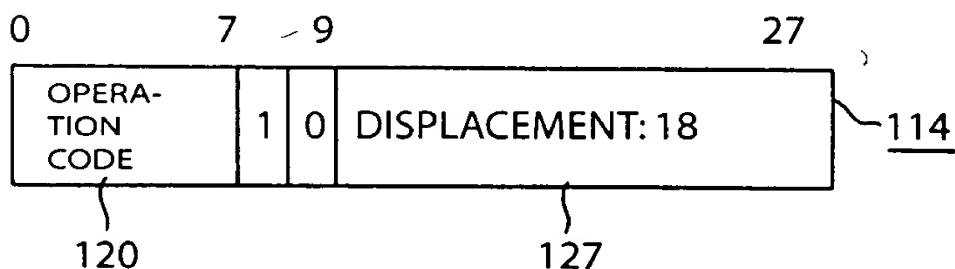
(123)
 X=00 => SOURCE=Rc
 X=01 => SOURCE=Rc; Rb++
 X=11 => SOURCE=Rc; Rb--
 X=1- => SOURCE= IMMEDIATE
 VALUE: 6



(123)
 X'=0=> SOURCE= Rc
 X'=1=> SOURCE=
 IMMEDIATE
 VALUE: 6



Ra,Rb,Rc = REGISTER NO.



(129)
 Y=0=> SOURCE= Rc
 Y=1=> SOURCE =
 IMMEDIATE
 VALUE: 12
 Z=0=> FOR TESTING
 ZERO
 Z=1=> FOR TESTING
 OTHER THAN ZERO

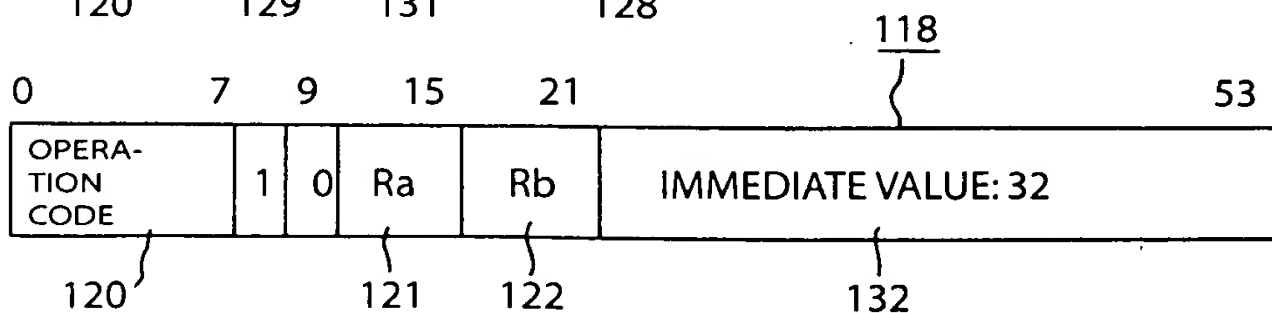
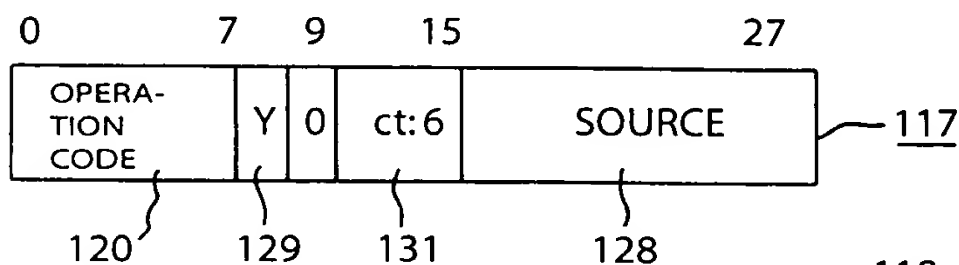
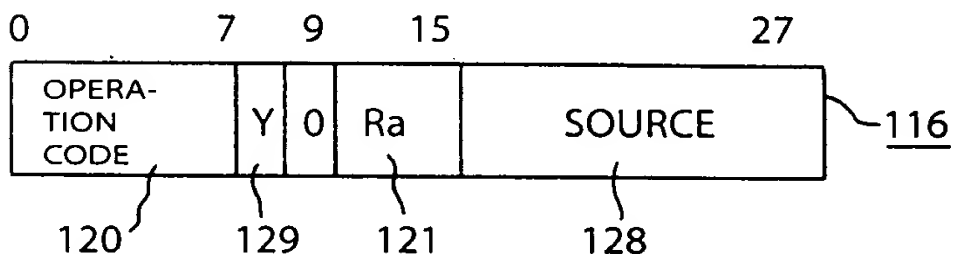
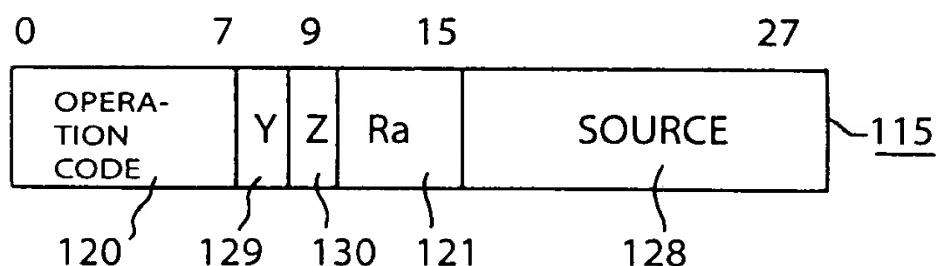


FIG. 5 (a)

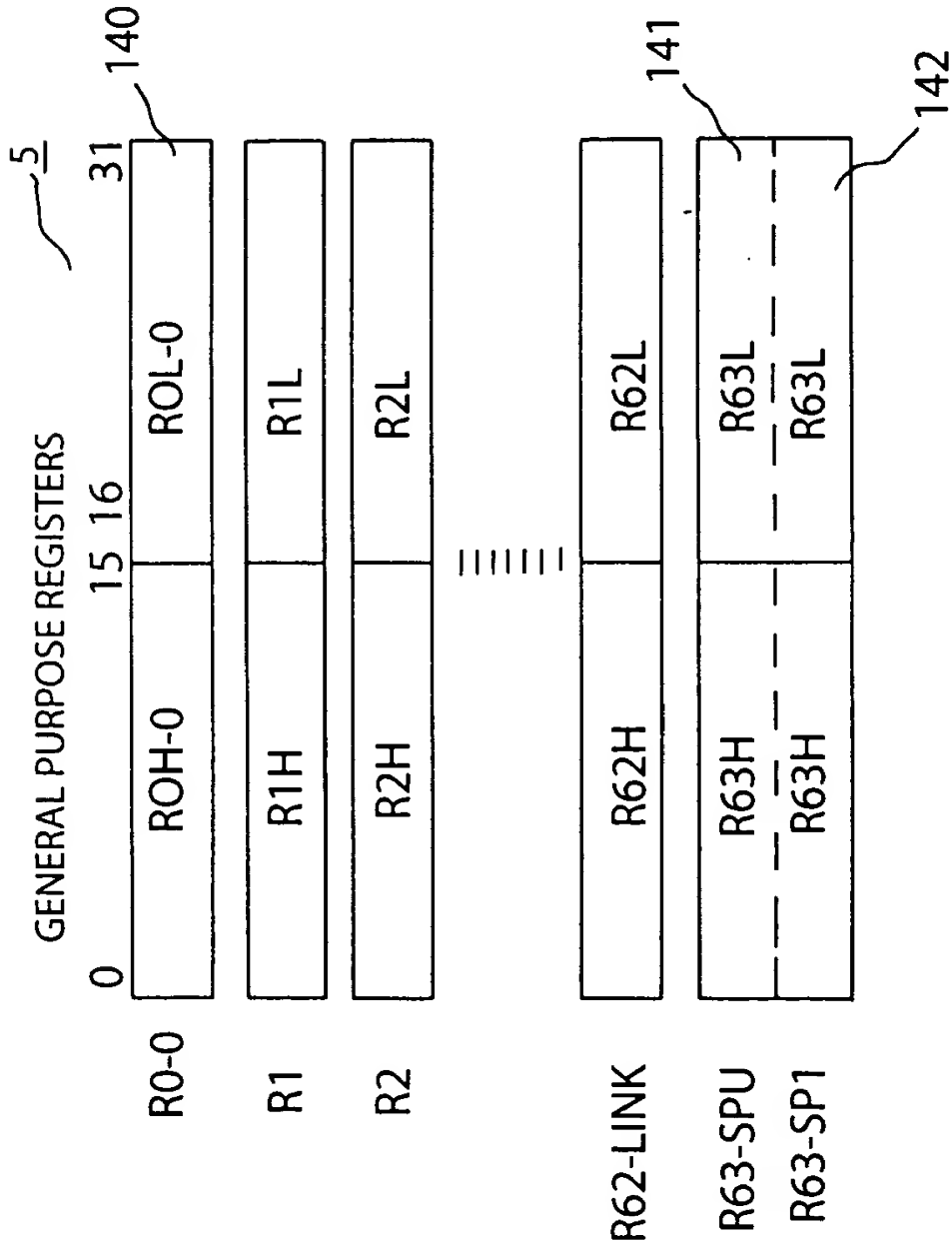


FIG. 5 (b)

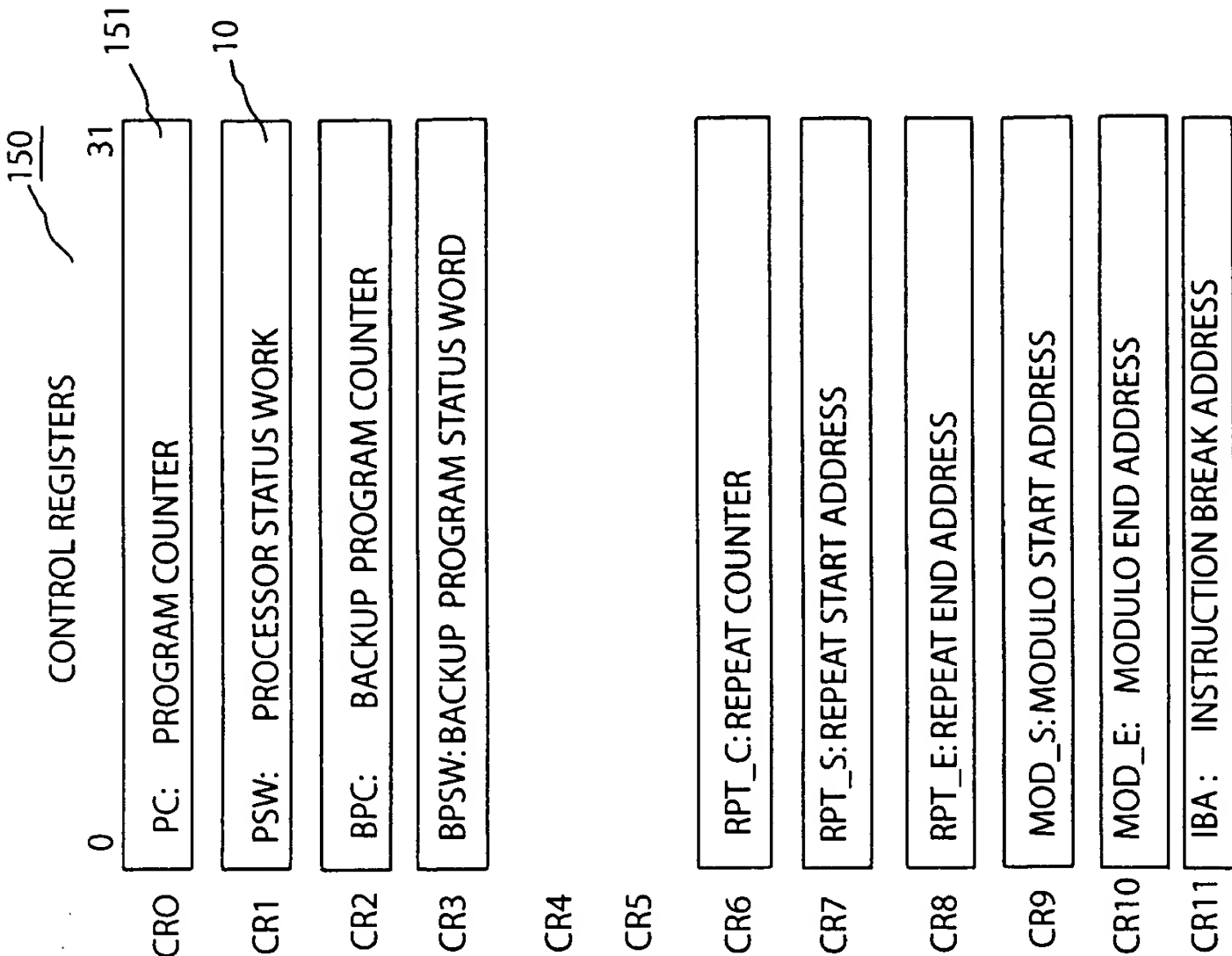


FIG. 5 (c)

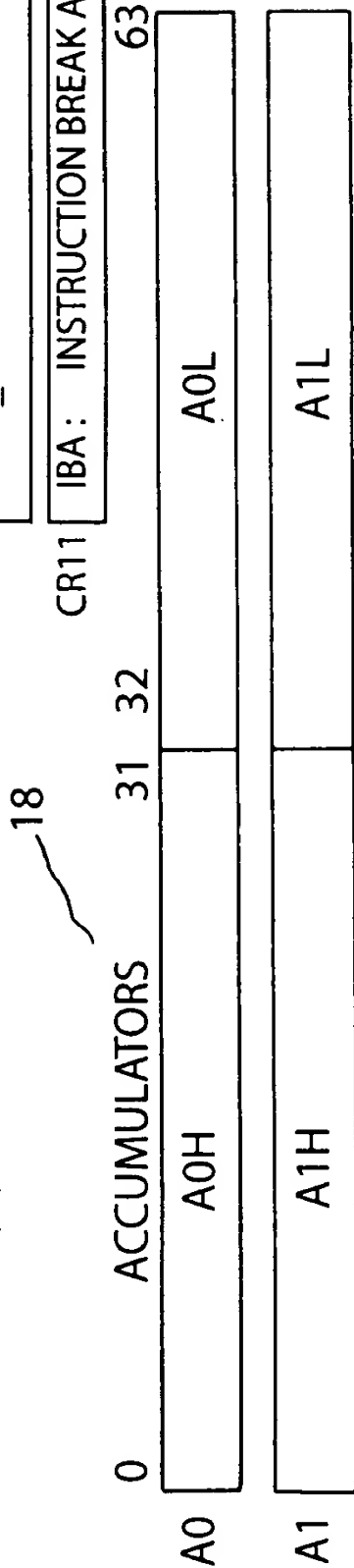


FIG. 6

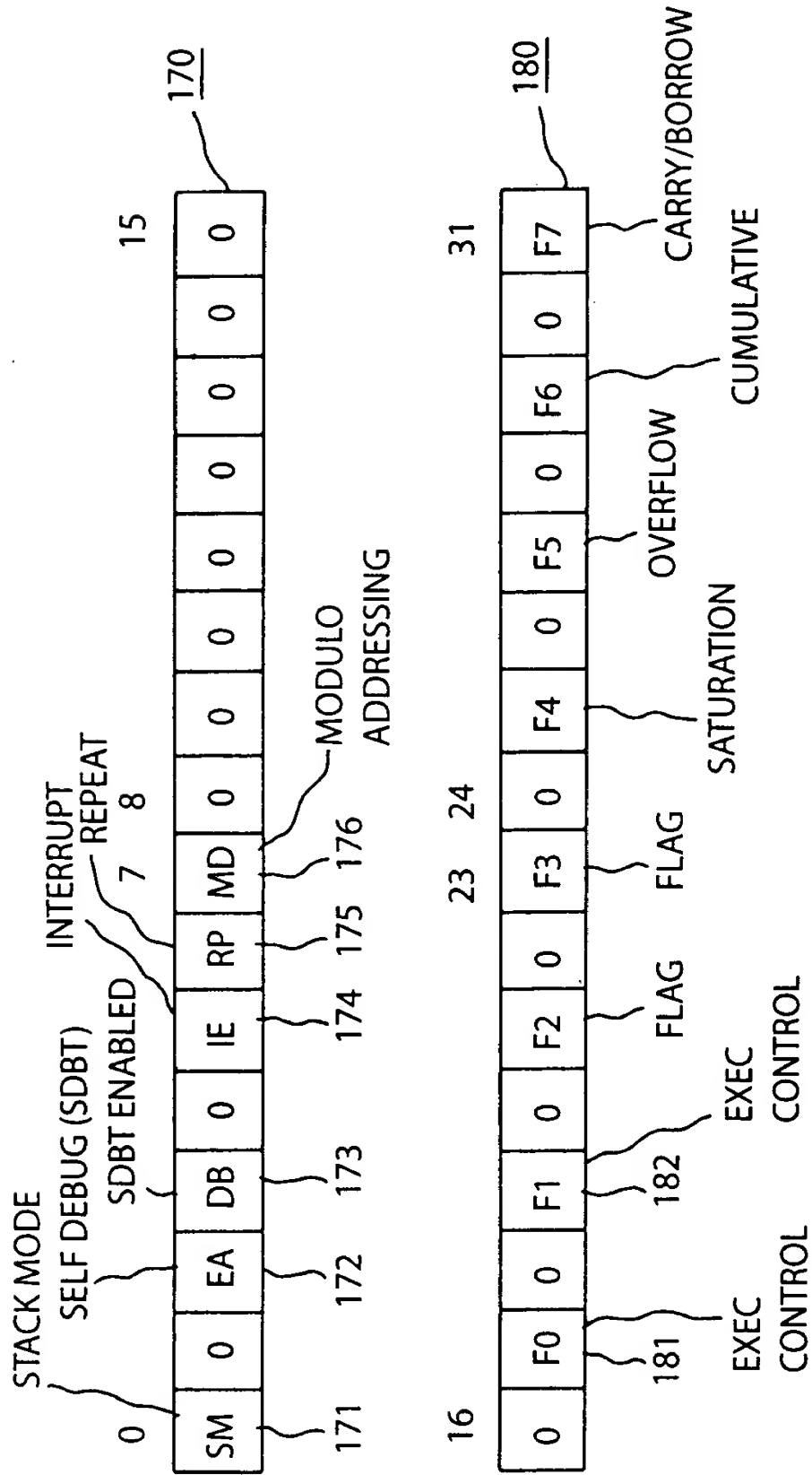


FIG. 7

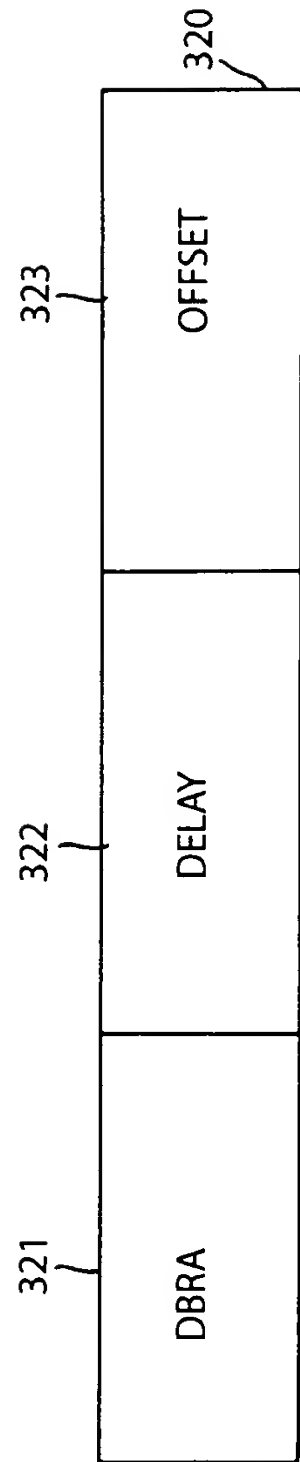


FIG.8

INSTRUCTION ADDRESS	OPERATION_0	OPERATION_1
H' 1000	I01 (BRA FOF #H'20 loop)	I02:
H' 1008	loop:I11	I12:
H' 1010	I21 (ADD R2,R2,R3)	I22:
H' 1018	I31 (CMPEO R2,R4,FO)	I32:
H' 1020	end:I41	I42:
H' 1028	I51	I52:
H' 1030	I61	I62:

CLOCK CYCLE	t1	t2	t3	t4	t5	t6	t7	t8	t9	t10	t11	t12	t13
VALUE OF PC	H'OFF0	H'OFFB	H'1000	H'1008	H'1010	H'1018	H'1020	H'1008	H'1008	H'1008	H'1010	H'1018	H'1020
I01(BRA)	IF	D/A					E/M						E/M
I02	IF	D/A	E/M	W			(JD)						(JD)
I11		IF	D/A	E/M	W								
I12		IF	D/A	E/M	W								
I21(ADD)													
I22			IF	D/A	E/M	W							
			IF	D/A	E/M	W							
I31(CMPEQ)													
I32				IF	D/A	E/M	W						
				IF	D/A	E/M							
I41					IF	D/A	E/M	W					
I42					IF	D/A	E/M	W					
I51													
I52						IF	D/A	E/M	W				
						IF	D/A	E/M	W				
I61													
I62							IF	D/A	E/M	W			
							IF	D/A	E/M	W			
I11													
I12													
								IF	D/A	E/M	W		
								IF	D/A	E/M	W		

JD: JUDGING
CONDITION

FIG. 9

FIG. 10

INSTRUCTION ADDRESS	OPERATION_0	OPERATION_1
H' 1000	I01 (DJMP FOT #H'18#H'28R5)	I02:
H' 1008	I11 (ADD R1, R2)	I12:
H' 1010	I21 (CMPEQ R1, R3, F0)	I22:
H' 1018	I31 (ADD R5, R5, R6)	I32:
H' 1020	I41	I42:
H' 1028	I51	I52:
H' 1030	I61	I62:
H' 1038	I71	I72:

FIG. 11

JD: JUDGING
CONDITION[illegible]

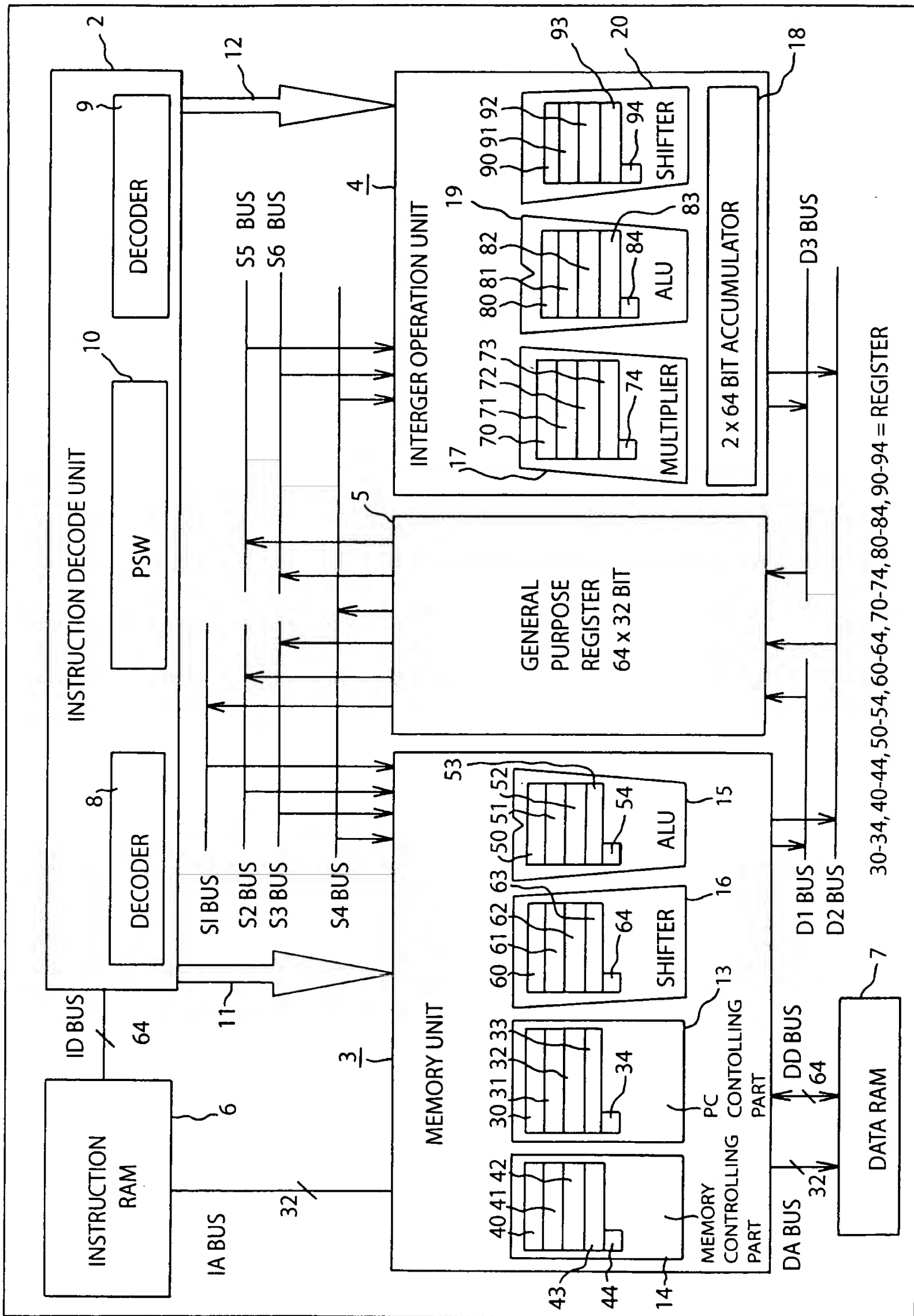


FIG. 12